



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Eric P. Traut et al.

Examiner: Wong, Leslie

Patent No.: 6968350

Group Art Unit: 2167

Issue Date: Nov. 22, 2005

Confirmation No.: 9879

Customer No.: 22971

Docket No: 304105.02

Title: METHOD FOR ESTABLISHING A VIRTUAL HARD DRIVE FOR AN EMULATED COMPUTER
SYSTEM RUNNING ON A HOST COMPUTER SYSTEM

Attn: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

We are transmitting herewith the attached:

- ☒ Request for Certificate of Correction (1 pg.).
- ☒ Certificate of Correction Form - PTO-1050 (2 pgs.)
- ☒ A return postcard.

Customer Number: 22971

By: /Sung T. Kim/
Attorney Name: Sung T. Kim
Reg. No.: 45,398

CERTIFICATE UNDER 37 CFR § 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Attn: Certificate of Correction Branch, Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450, on this 20th day of August 2009.

Christine Hartness

Christine Hartness

Patent 6968350

PATENT

IN UNITED STATES PATENT AND TRADEMARK OFFICE

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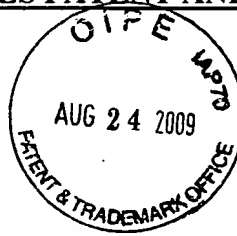
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REQUEST FOR CERTIFICATION OF CORRECTION

Attn: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

As the error is that of the Patent Office, it is believed that no fee is due.

Respectfully submitted,

Customer Number: 22971

Certificate

AUG 26 2009

of Correction

Date: June 23, 2009

By: /Sung T. Kim/

Attorney Name: Sung T. Kim

Reg. No.: 45,398

CERTIFICATE UNDER 37 CFR § 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Attn: Certificate of Correction Branch, Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450, on this 20th day of August 2009.

Christine Hartness
Christine Hartness

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 6968350

DATED : Nov. 22, 2005

INVENTOR(S) : Eric P. Traut et al.

It is certified that errors appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the face page, in field (56), under "U.S. Patent Documents" in column 1, line 1, below
"4,779,188 A 10/1988 Gum et al. 364/200" insert - - 4,875,186 10/1989
Blume, Jr. 364/900 - -.

On the face page, in field (56), under "U.S. Patent Documents" in column 1, line 4, below
"5,367,628 A 11/1994 Ote et al. 345/501" insert - - 5,406,644 4/1995
MacGregor 395/500 - -.

On the face page, in field (56), under "Foreign Patent Documents" in column 2, line 1, below
"EP 0 645 701 A2 9/1994 G06F 9/455" insert - - EP 0 524 773 A1 7/1992
..... G06F 9/44 - -.

On the face page, in field (56), under "Foreign Patent Documents" in column 2, line 2, below
"EP 0 645 701 A2 9/1994 G06F 9/455" insert - - WO WO 98/57262
12/1998 - -.

On page 2, in field (56), under "Other Publications" in column 2, line 3-4, delete
"MICROPROCESSORS/32 BIT" and insert - - MICROPROCESSORS/32_BIT - -, therefor.

On page 2, in field (56), under "Other Publications" in column 2, line 10, delete
"MICROPROCESSOR" and insert - - MICROPROCESSORS - -, therefor.

On page 2, in field (56), under "Other Publications" in column 2, line 11, delete "MCPC7XX"
and insert - - MPC7XX - -, therefor.

On page 2, in field (56), under "Other Publications" in column 2, line 13, delete "filiind" and
insert - - filing - -, therefor.

MAILING ADDRESS OF SENDER:

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PATENT NO. 6,968,350

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➡ Page 5 of 6

Atty Docket No: 304105.02

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PATENT NO : 6968350

DATED : Nov. 22, 2005

INVENTOR(S) : Eric P. Traut et al.

It is certified that errors appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On page 2, in field (56), under "Other Publications" in column 2, line 21, delete "0006!-0007!" and insert - - '0006!-'0007! - -, therefor.

On page 2, in field (56), under "Other Publications" in column 2, line(s) 28-30, below "Shang Rong Tsai et al., On the Architectural Support for Logical Machine Systems, Microprocessing and Microprogramming, vol. 22, No. 2, pp. 81-96, Feb. 1988." insert - - **PCT International Search Report in International Application No. PCT/US 01/22277, International filing date 7/16/01, mail date 2/7/02.** - -.

In column 9, line 34, in Claim 1, after "system" delete ";;".

In column 10, line 3, in Claim 3, after "system" insert - - **at the conclusion of a computing session** - -.

In column 10, line 4, in Claim 3, insert - - virtual - - before "hard drive".

In column 10, line(s) 5-6, in Claim 3, after "system" delete **"at the conclusion of a computing session"**.

In column 10, line 10, in Claim 3, delete "virtual" before "differencing drive".

In column 10, line 11, in Claim 3, insert - - virtual - - before "hard drive".

In column 10, line 12, in Claim 3, after "of the" delete "virtual".

In column 10, line 37, in Claim 5, after "system" insert - - **at the conclusion of a computing session** - -.

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